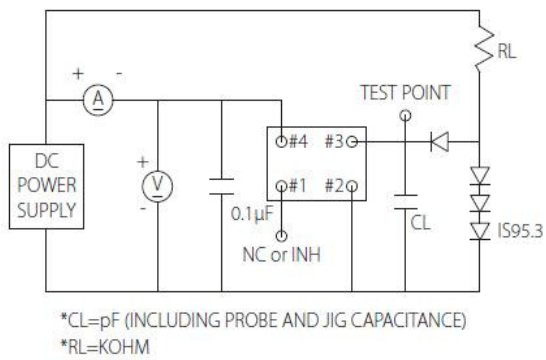


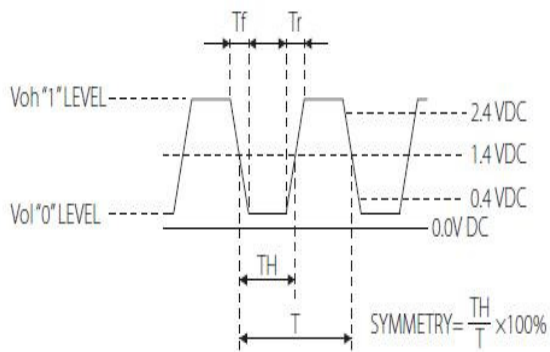
Test circuit of TTL output & Clipped Sine output

OSCs test circuit of TTL output

Model: OSC52, OSC71



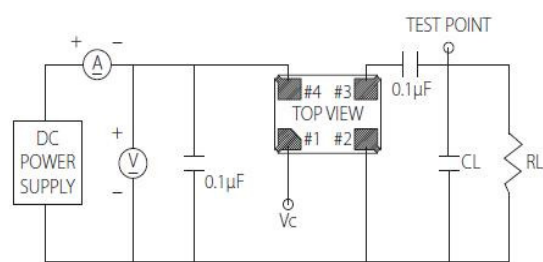
OUTPUT WAVEFORM



TTL	Ro	Remarks
2TTL	2k Ω	CL=15pF (Probe Capacitance)
5TTL	820 Ω	
10TTL	390 Ω	

TCXOs and VC-TCXO test circuit of Clipped Sine output

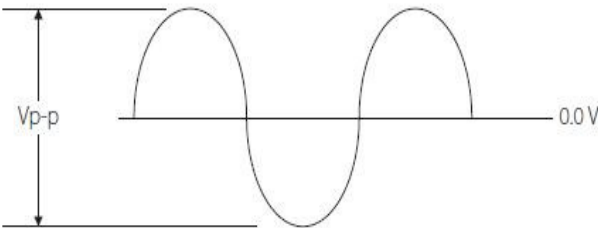
Model: TXO83, VTX83, TXO92, VTX92



*CL=pF (INCLUDING PROBE AND JIG CAPACITANCE)

*RL=KOHM

OUTPUT WAVEFORM



Load ZL	Remarks
10kΩ//10pF	Including Probe Capacitance & Fixture stray capacitance

	#1 Vcont
TCXO	0.0V (GND)
VC-TCXO	Vcont=+1.5±1.0V